

REMARKS

The Office Action of 03/24/2008 has been carefully considered. Reconsideration in view of the present remarks is respectfully requested.

Claim 7 was indicated as containing allowable subject matter, which indication is appreciatively acknowledged.

Claims 1, 3, 5, 6 and 8-14 were rejected as being unpatentable over Watanabe in view of Cairns. Claims 15-20 were rejected as being unpatentable over the same base combination further in view of Hung. These rejections are respectfully traversed and reconsideration requested.

The rejection states in part:

[C]airns teaches digital synthesizer driver phase locked loop comprises, in said modulating state, a first filtering performance, with said digital synthesizer drive phase locked loop comprising, in said oscillating state, a second filtering performance different from said first filtering performance (col. 9 line 64 through col. 10 line 19 and col. 14 lines 46-57).

Applicant respectfully disagrees.

Referring to Fig. 5 of Cairns, the main synthesizer 206a produces an oscillation signal that is used for both transmission and reception. Presumably, this is the structure considered to be the “single digital synthesizer driven phase locked loop” claimed. Note that an output signal of the amplifier 214 is applied to an amplifier (unlabeled) to produce the signal RXLO1, and is also applied (through an unlabeled attenuator ATT) to the transmit modulator 100. However, in both instances, *the filtering performance*, as determined by the loop filter 210, *is identical*. It may be seen therefore that Cairns does

not teach or suggest a digital synthesizer driven phase locked loop comprising, in a modulating state, a first filtering performance, with said digital synthesizer drive phase locked loop comprising, in an oscillating state, a second filtering performance different from said first filtering performance, as claimed.

Other loop filters besides the loop filter 210 are described in Cairns, including loop filter 218, loop filter 194, and loop filter 304. These loop filters, however, are believed to be irrelevant to the claimed invention, insomuch as the claimed invention relates to a *single* digital synthesizer driven phase locked loop having a modulating state and an oscillating state.

Withdrawal of the rejection and allowance of claims 1, 3 and 5-20 is respectfully requested.

Respectfully submitted,

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